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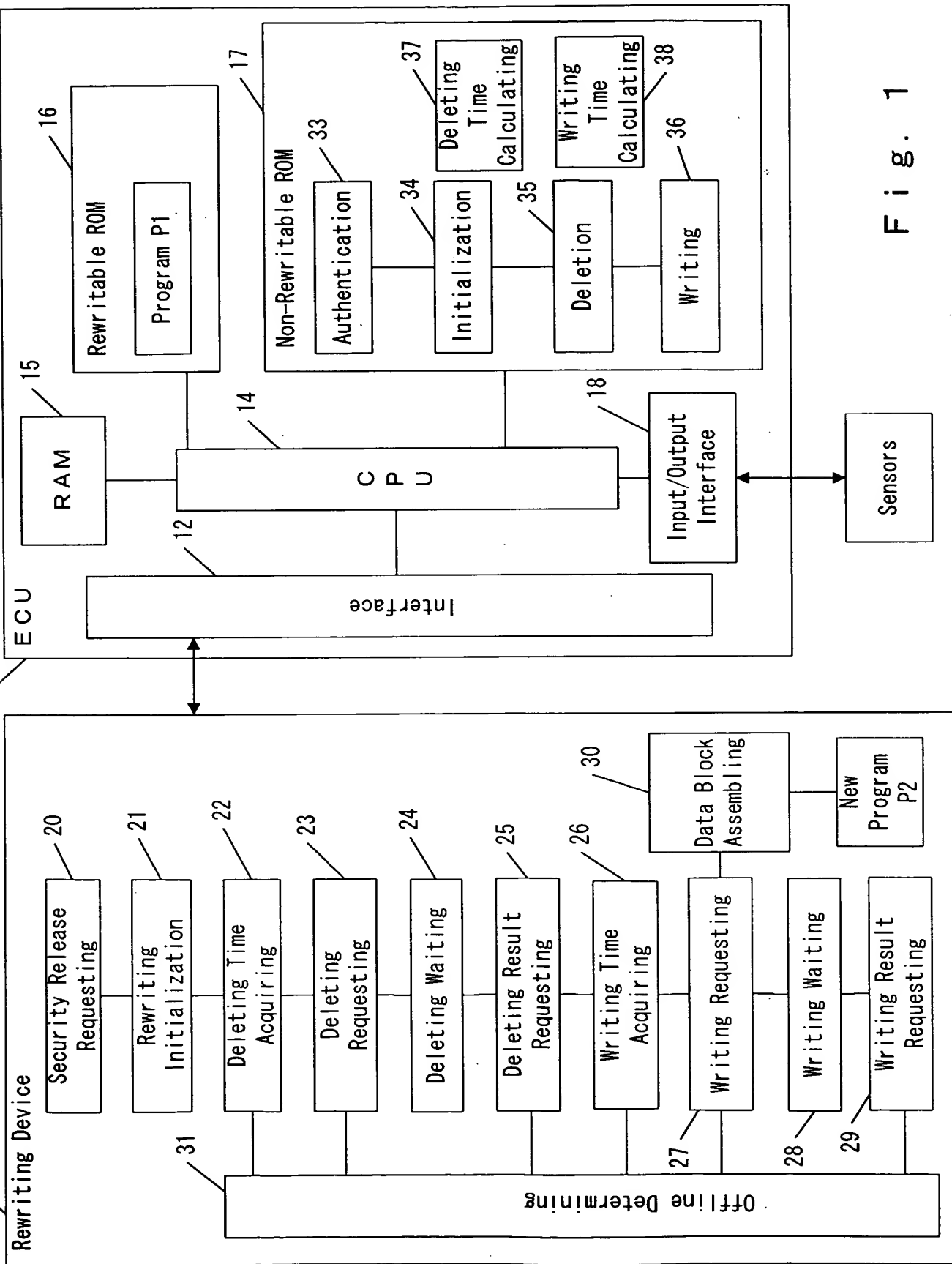
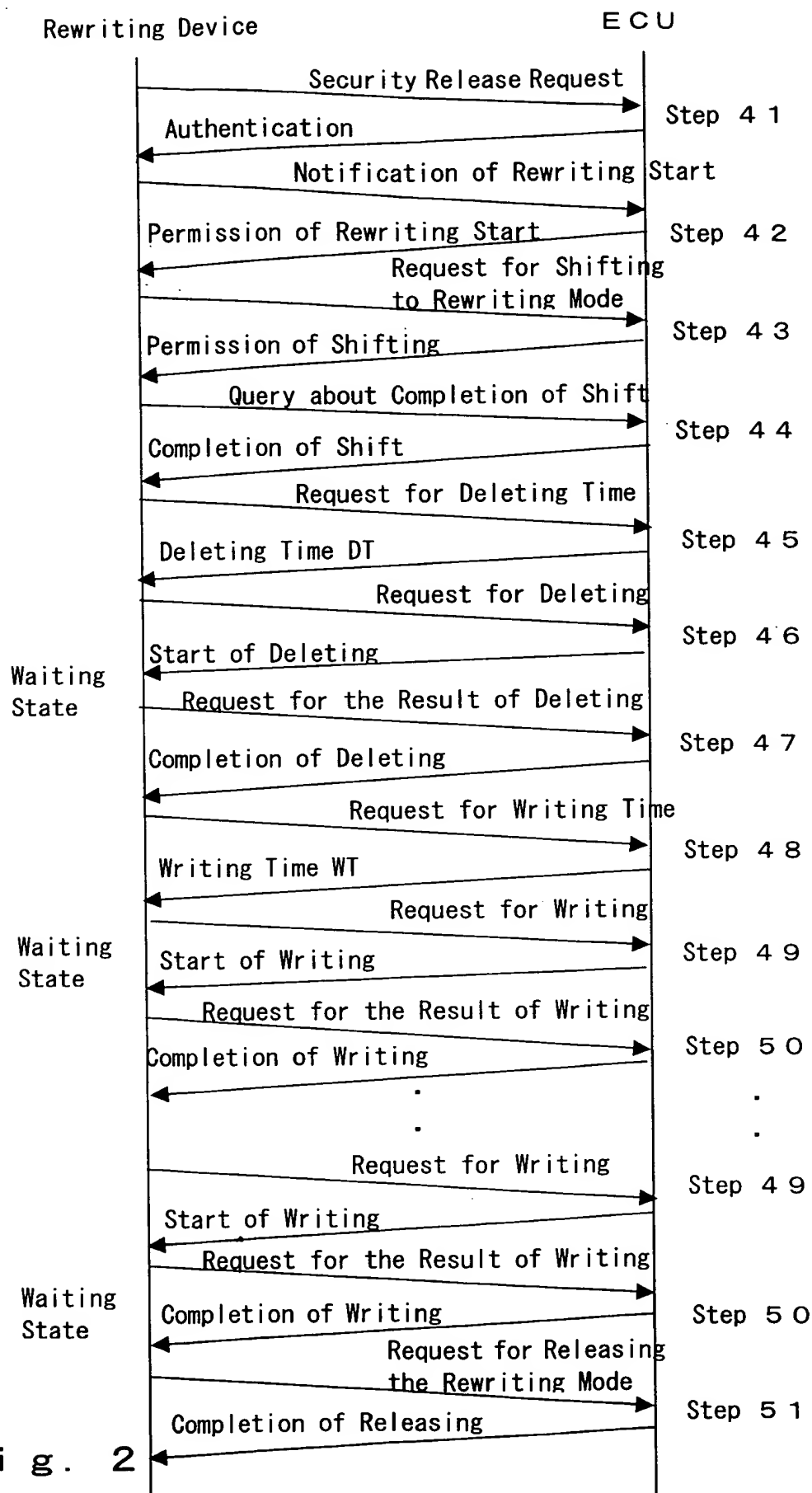


Fig. 1



**Fig. 2**

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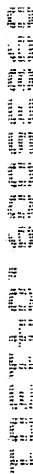
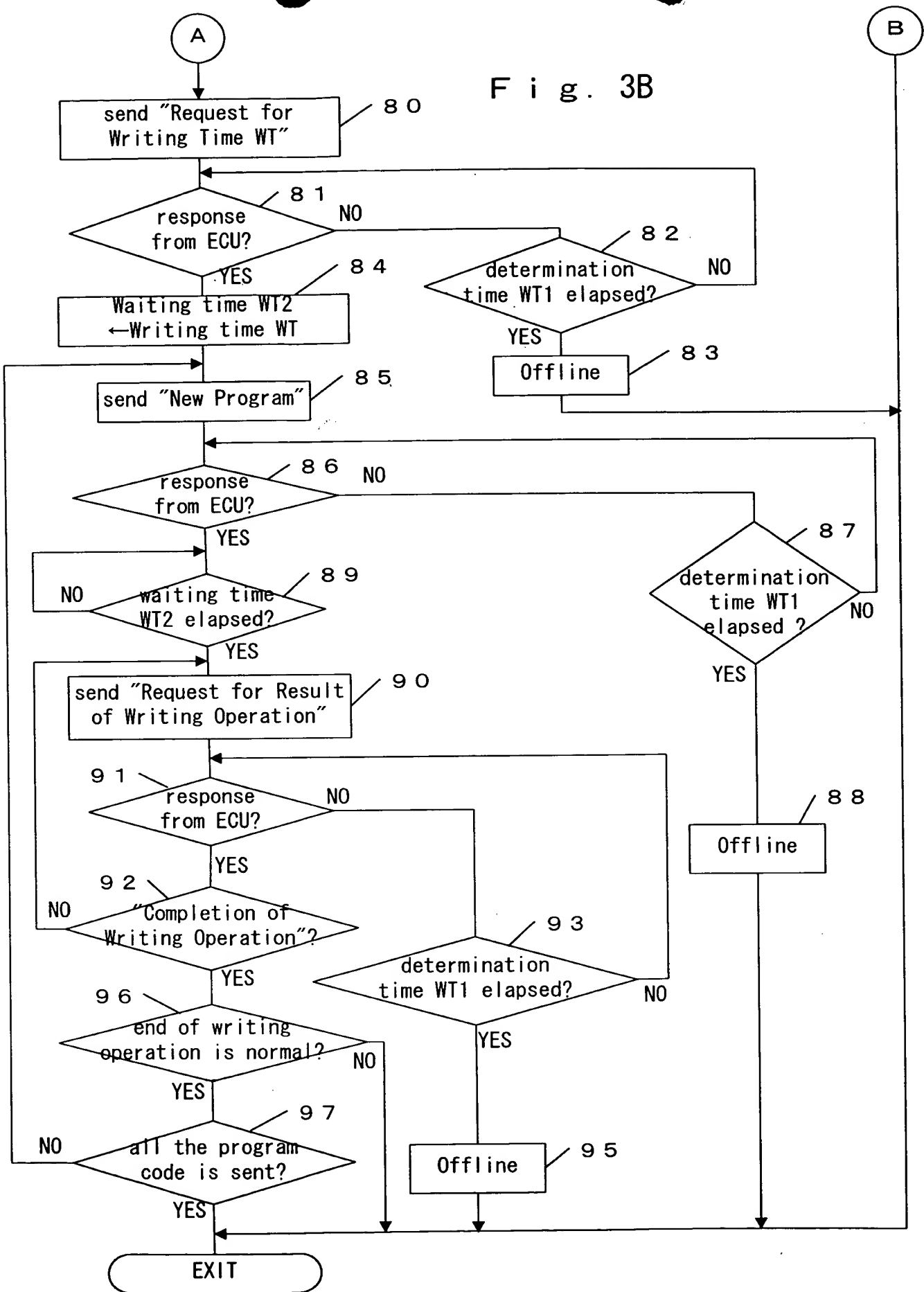
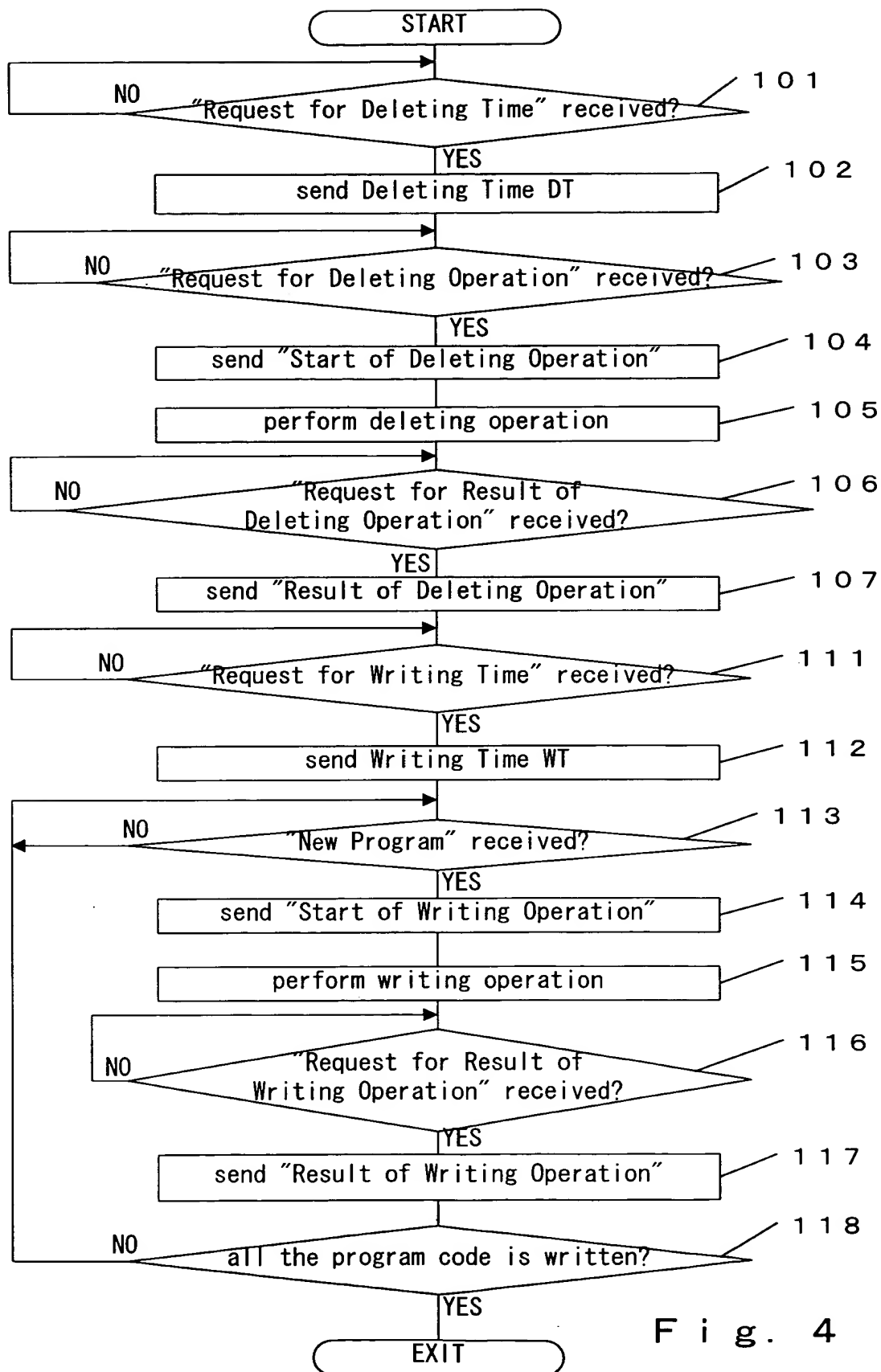


Fig. 3B





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Fig. 5

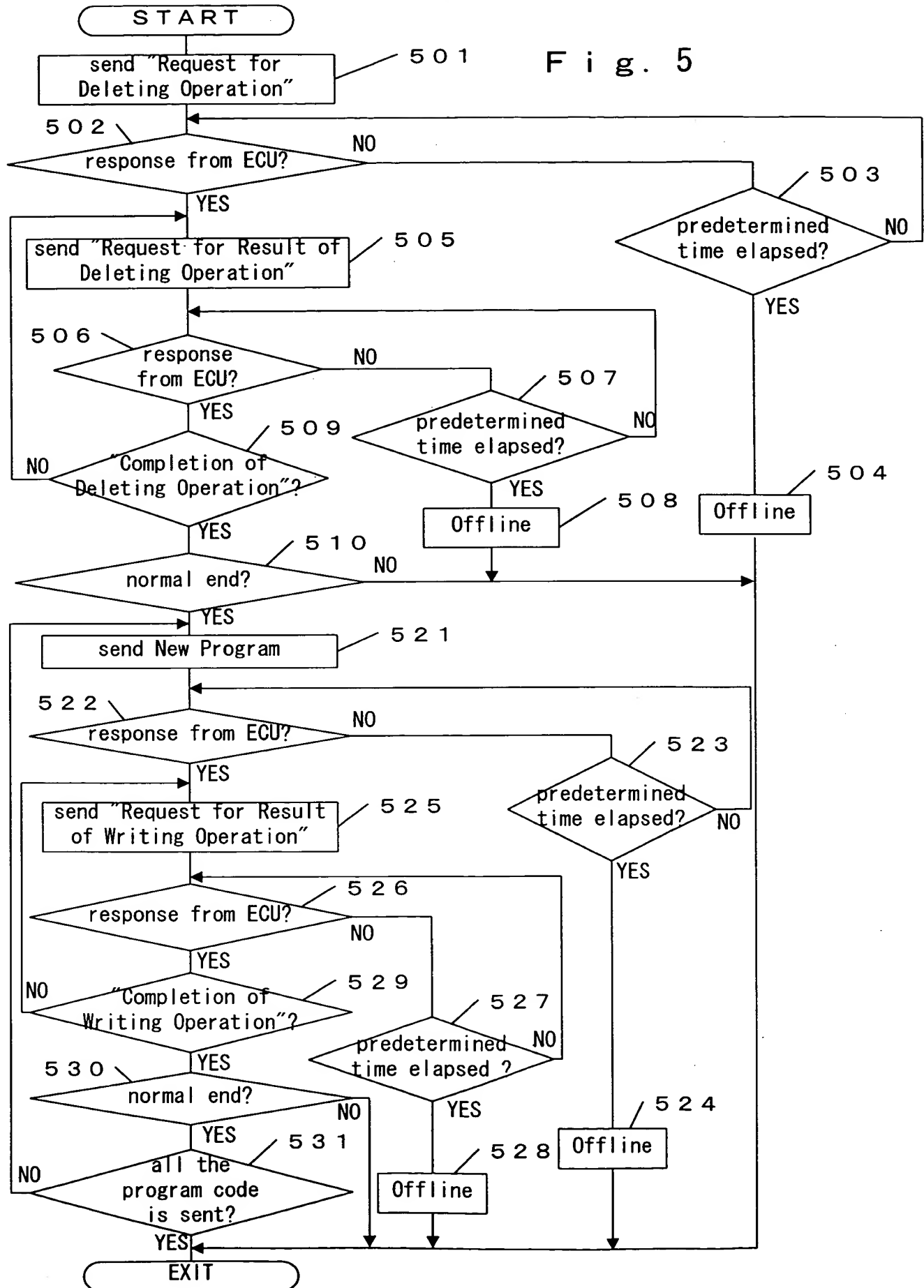


Fig. 6

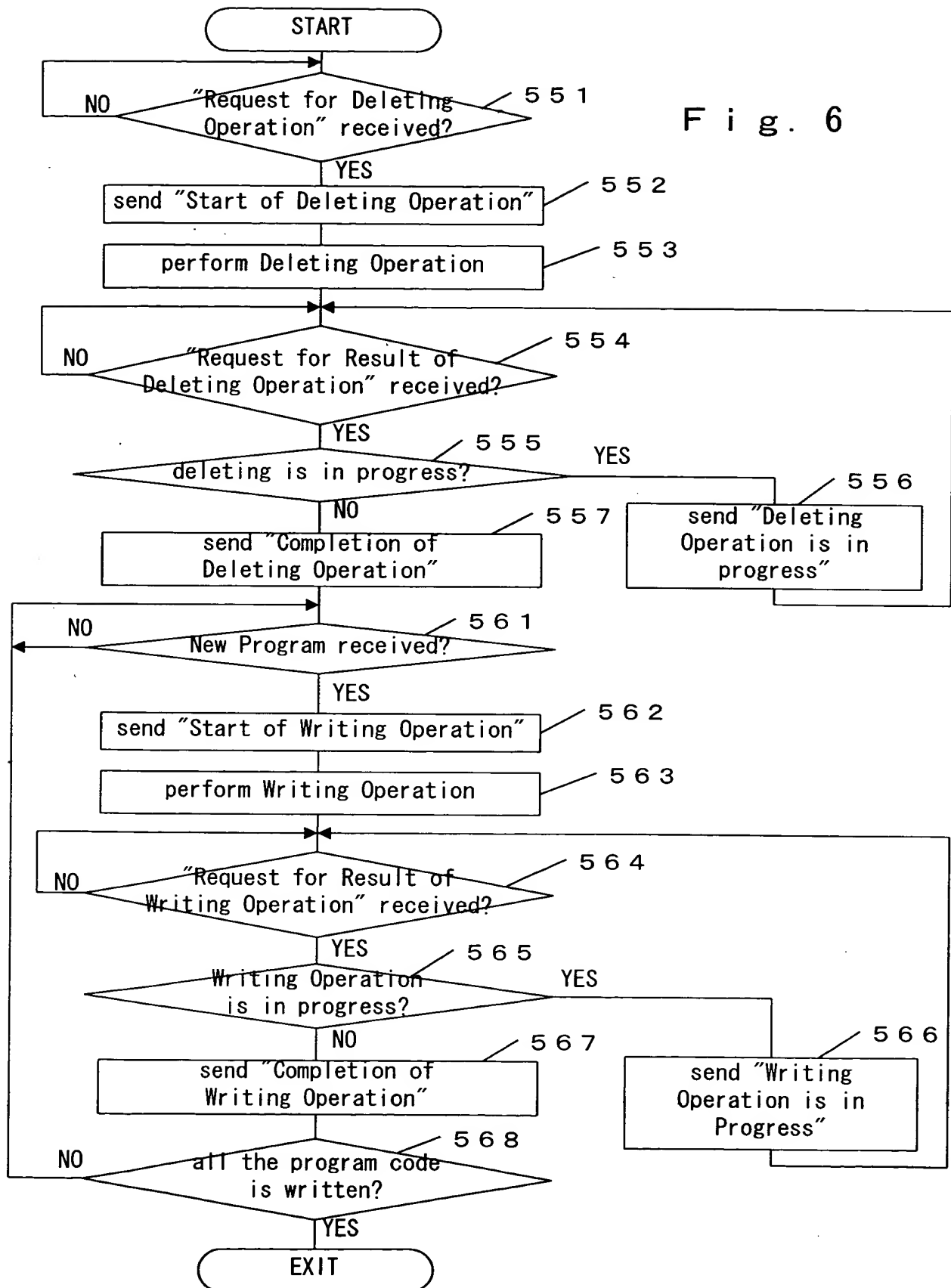


Fig. 7 is a block diagram of a system for rewriting a program stored in a non-volatile memory of an ECU. The system includes a rewriting device 211, an ECU 210, and a CPU 214. The ECU 210 includes an interface 212, a CPU 214, and a flash memory 216. The CPU 214 is connected to the interface 212 and the flash memory 216. The rewriting device 211 is connected to the interface 212. The CPU 214 is also connected to the flash memory 216. The CPU 214 is also connected to the flash memory 216. The CPU 214 is also connected to the flash memory 216.

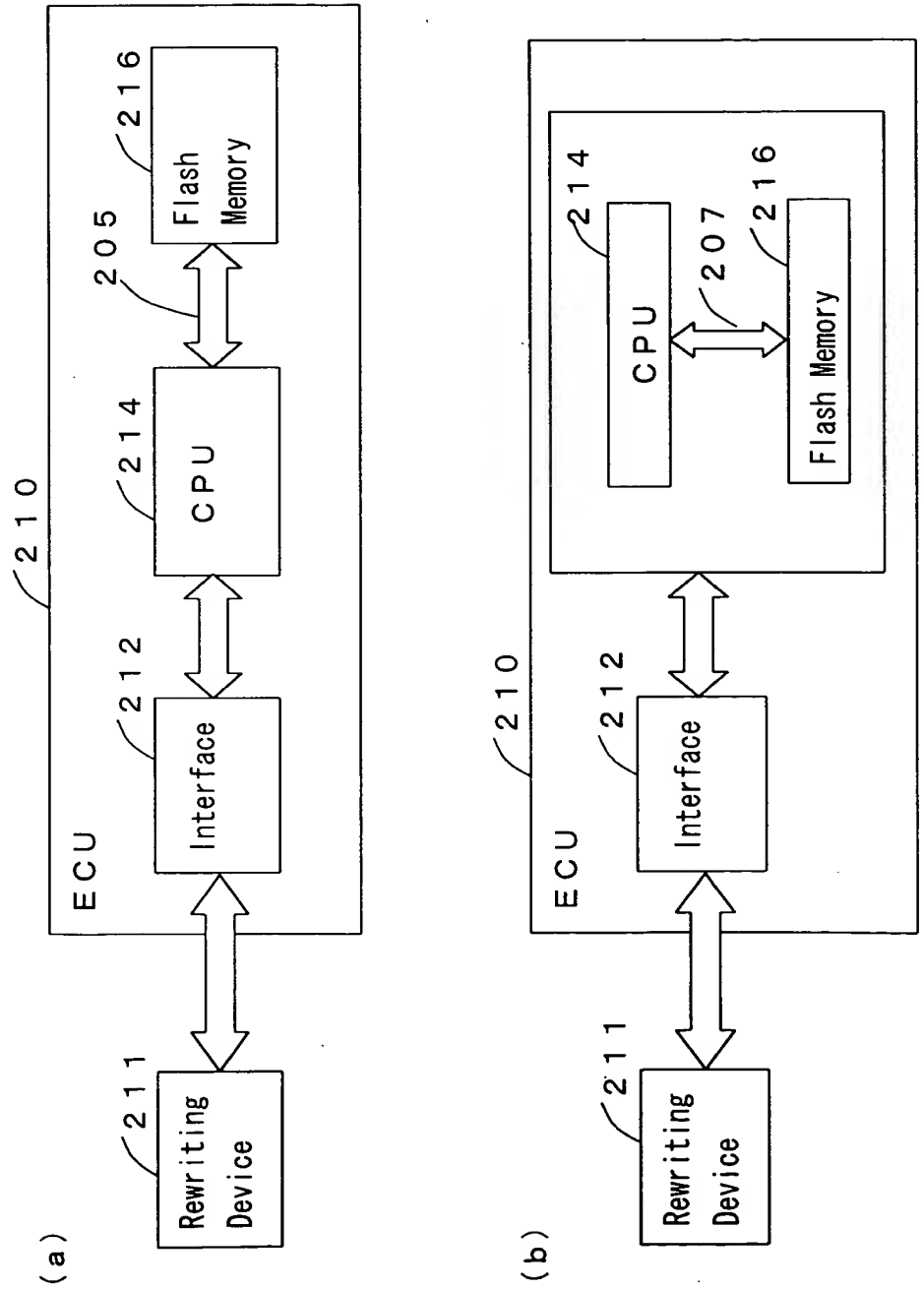


Fig. 7